

	A	B	C	D	E
1	Perm: 123456	Name: Joe Gaucho Major: CE	EAP Host University: Lund University, Sweden	Term on EAP: F23	
2	UCSB Course or requirement	EAP Course Name and number	Course Description	Link to University Course Page	Faculty Assessment
3	ECE 154A	EITF 20 Computer Architecture	The goal of this course is to introduce the operation of computer systems at the level of Instruction Set Architectures (ISA). It aims to provide a basic understanding of the design principles that govern modern computer architectures and their components. Especially it pays attention to (super scalar) pipelining and memory hierarchy techniques including caches. Implementation and efficiency issues are exemplified. Metrical performance analysis methods are discussed to evaluate architectural alternatives.	https://kurser.lth.se/lot/course-syllabus/22_23/EITF20	Approved as ECE 154A substitute.
4	ECE 178 - Introduction to Digital Image Processing	FMAN20 - Image Analysis	The main aim of the course is to give a basic introduction to theory and mathematical methods used in image analysis, to an extent that will allow the student to handle industrial image processing problems. In addition the aim is to help the student develop his or her ability in problem solving, both with or without a computer. A further aim is to prepare the student for further studies in e.g. computer vision, multispectral image analysis and statistical image analysis.	https://kurser.lth.se/kursplaner/17_18%20eng/FMAN20.html	Denied.
5	CMPC 153A - Hardware/Software Interface	EDAF55 - Concurrent Programming	Computers are increasingly subcomponents in technical equipment where they must interact with and control external physical processes in real time. The course gives an introduction to how such computer systems work and gives practical experience from programming such systems.	https://kurser.lth.se/kursplaner/17_18%20eng/EDAF55.html	Approved as CE elective. Acceptable substitute for CMPC 153A.
6	ECE156B - CAD of VLSI Circuits	EITF35 - Introduction to Structured VLSI Design	General introduction to large-scale digital integrated circuit design with emphasis on FPGA implementation. Efficient design-flow using modern CAD tools. Design methodology for synchronous logic. Modeling with synthesizable VHDL. Rapid prototyping using PPGA.	https://kurser.lth.se/kursplaner/17_18%20eng/EITF35.html	Denied.
7	GE Area GE E	SASH02 Archaeology: Viking Age Scandinavia	The course gives a broad overview of the archaeology, history, and art history of the 8th to the 12th centuries. The course deals with various aspects of political, social, cultural and religious development and change within the Scandinavian Viking Age. These include aspects such as the transition from pre-Christian religion to Christianity, funeral customs, gender and social segregation, trade and plunder, rural landscapes, and urbanization, and economic development.	https://www.ark.lu.se/kurs/SASH02/	Approved 1 GE Area G.
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9	THIS IS A SAMPLE ONLY. APPROVALS AND COURSES ARE NOT TO BE TAKEN AS APPROVALS FOR EAP PLANNING.				
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